



Verification Beyond Coverage

DAC 2023 Panel

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Agenda

- Coverage-driven Verification is everywhere
- It's all about #s – have we lost focus?
- What is beyond Coverage?

1. Short initial presentations
2. Questions !
3. Let's have fun

Adnan Hamid

Breker

- Adnan Hamid, Founder and CTO, Breker Verification Systems
- Adnan has spent some 30 odd years researching how engineers' reason about and represent verification knowledge and developing strategies for automating test scenario generation from these representations.
- His prior roles include managing AMD's system logic division, leading AMD's microprocessor verification group and serving as subject matter expert in System Level Verification at Cadence.
- He received BS degrees in Electrical Engineering and Computer Science from Princeton University and an MBA from the University of Texas at Austin.

David Lacey

HP

- Master's Degree in EE from Louisiana Tech University
- Working in system design for over 30 years
- Experience spans from embedded systems specification and design, hardware design, and FPGA and ASIC design and verification
- Currently serves as Chief Verification Scientist at Hewlett Packard Enterprise where he helps shape VLSI methodologies across HPE

Mark Glasser

Elastics

- Mark has been in the EDA and semiconductor industries for more than 35 years. He has worked developing tools as well as developing and deploying methodologies.
- Mark is one of the original architects of UVM and is the author of the OVM Cookbook. He is also the co-author of two patents.
- Mark holds a Bachelor of Science Degree from California State University, Northridge.
- Currently, Mark is a Principal Verification Architect at Elastics.cloud, a company focused on developing CXL-based products for reducing costs and improving utilization of data centers

Matt Graham

Cadence

- Matt Graham is a Product Management Group Director in the System and Verification Group at Cadence, working with products like the Xcelium logic simulator and the Verisium AI-Driven Verification Platform.
- Matt began his career as a verification engineer almost 25 years ago, before moving on to various Applications and Product engineering roles during his now almost 20 years at Cadence.
- Matt has always worked closely with coverage and coverage closure technologies from Specman, to UVM, to Cadence vManager regression management tools and now Xcelium and Verisium.
- Matt is originally from Canada, but is now based out of Cadence's international headquarters in Cork, Ireland